	Application No.	Applicant(s)	
Serial Number	10/617,706	RHODES, HOWARD E.	
1 (ERIE) ([A]) 55 (16 5)(A) (15 (16 6)) 60 (16 6)) 60 (17 6)			

TERMINAL DISCLAIMER ⊠ APPROVED □ DISAPPROVED				
The term of this patent shall not	6,611,013			
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The term of this patent subsequent to the adjacent date has been disclaimed.				
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**Docket Number (Optional)** 

M4065.0100/P100-B

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In re Application of:

Howard E. Rhodes

Application No.: 10/617,706-Conf. #3719

File	d: July 14, 2003	
For	CMOS IMAGER WITH SELECTIVELY SILICIDED GATES	
inst inst as t by a only	owner*, MICRON TECHNOLOGY, INC. , of 10 ant application hereby disclaims, except as provided below, the terminal part of the statutory term ant application which would extend beyond the expiration date of the full statutory term of prior pate the term of said prior patent is defined in 35 U.S.C. 154 and 173, and as the term of said prior pany terminal disclaimer. The owner hereby agrees that any patent so granted on the instant application and is binding upon the grantee, its successors or assigns.	ent No. 6,611,013 patent is presently shorteneral plication shall be enforceable
ann	naking the above disclaimer, the owner does not disclaim the terminal part of the term of any p dication that would extend to the expiration date of the full statutory term as defined in 35 U.S. ent, "as the term of said prior patent is presently shortened by any terminal disclaimer," in the	C. 154 and 173 of the prior
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2.	The undersigned is an attorney or agent of record. Reg. No	_
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Terminal disclaimer fee under 37 CFR 1.20(d) is included.

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11/17/2004 ADDF01 00000097 10617706

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